

PCN Number:	20180215000A	PCN Date:	March 12, 2018
Title:	Datasheet for DRV10983, DRV10983Z		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.

TI is disabling Inductive AVS in default EEPROM.

Warning: Customers can enable Inductive AVS, but TI recommends not to enable feature, because it is not capable of preventing voltage surge in all cases.

TI strongly recommends new typical application schematics provided for Z (sleep mode) device.



DRV10983, DRV10983Z

SLVSCP6F – JULY 2014 – REVISED DECEMBER 2017

Changes from Revision E (May 2017) to Revision F

Page

• Added the internal SPEED pin pulldown resistance to ground parameter to the <i>Electrical Characteristics</i> table	8
• Changed the <i>Step-Down Regulator</i> section	12
• Updated the <i>Motor Phase Resistance</i> section	15
• Deleted the <i>Inductive AVS Function</i> section	35
• Changed the default value for register address 0x29 from 0xB7 to 0xB9 in the <i>Default EEPROM Value</i> table	41
• Added application information for the sleep mode device	46



DRV10983, DRV10983Z

SLVSCP6G – JULY 2014 – REVISED FEBRUARY 2018

Changes from Revision F (December 2017) to Revision G

Page

• Added timing information for entering and exiting sleep mode and standby mode	8
• Added BEMF COMPARATOR hysteresis specification	9
• Updated <i>Start the Motor Under Different Initial Conditions</i> figure	20
• Changed the default value for register address 0x27 from 0xFC to 0xF4 in the <i>Default EEPROM Value</i> table	42
• Deleted the "TI recommends..." sentence from the description for address 0x27, bit 3	45
• Added constraints to recommended external inductor	48

The datasheet number will be changing.

Device Family	Change From:	Change To:
DRV10983, DRV10983Z	SLVSCP6E	SLVSCP6G

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/DRV10983>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

DRV10983PWP	DRV10983PWPR	DRV10983ZPWP	DRV10983ZPWPR
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For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com