

End of Life Notification for CP2120 SPI-to-I2C Bridge Devices

EOL Issue Date: 12/15/2020

Effective Date: 12/21/2021

Last Order Date: 6/21/2021

PCN Type: Discontinuance

Description of Change

Silicon Labs announces an End of Life (EOL) and Last Time Buy (LTB) for CP2120 SPI-to-I2C Bridge Devices.

Existing customers may place LTB orders per the key dates above and guidelines listed in this EOL form.

All customer orders with a Customer Requested Delivery Date (CRD) between the Last Time Buy (Order) Date and the EOL Effective Date will be Non-Cancellable and Non-Returnable (NCNR). As of this EOL Date, Silicon Labs will no longer accept customer orders with a CRD beyond the EOL Effective Date.

If you have questions about this EOL, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at www.silabs.com.

Reason for Change

Discontinuance of technical support capability and infrastructure.

Impact on Form, Fit, Function, Quality, Reliability

None.

Product Identification

Existing Part #	Replacement	Part #	DropInCompInd.
CP2120-GM	NA		No
CP2120-GMR	NA		No

Kit Identification

Orders for the following obsolete kits will no longer be accepted.

Existing Kit # Replacement Kit # NA NA

Customer Response

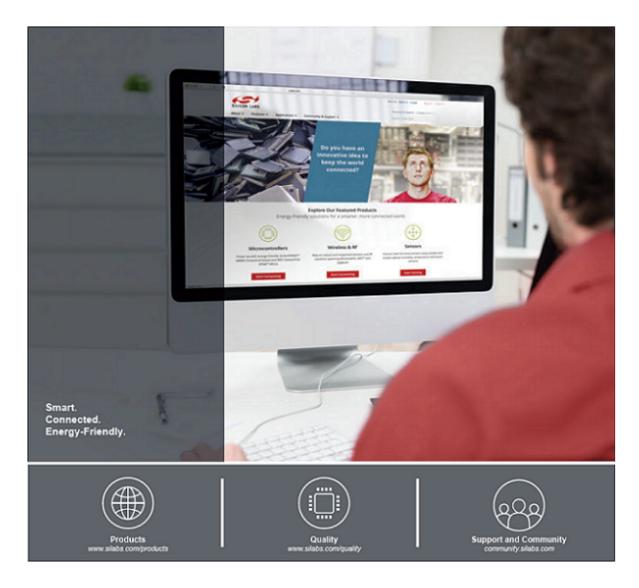
Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at http://www.silabs.com.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users. <u>http://www.silabs.com/profile</u>



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Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized to key set on the ass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

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