| PCN Nun | | | | | | | | |
|--|---|---|----------------|---|--|---------------------------|----------------------------------|--|
| | | | 20170 | .70424002A PC | | | | May 5, 2017 |
| Title: | Qualit devic | | f addit | cional Fab site (R | FAB) and Assem | bly sit | te (ASE | N) option for select |
| Customer Contact: | | <u>P</u> | PCN Manager | | Dept: | | Quality Services | |
| Proposed 1 st Ship Date: | | J | uly 26, 2017 | Estimate Availabil | ated Sample ability: | | Date provided at sample request. | |
| Change Type: | | | | | · | _ | | |
| Asse | | | | Assembly Process | | Assembly Materials | | nbly Materials |
| Desig | | | | Electrical Sp | | Mech | | anical Specification |
| | | | | | oping/Labeling | | | Process |
| | r Bump | | | Wafer Bump | | 12 | Wafer Bump Process | |
| ⊠ Wafe | r Fab Si | ite | | Wafer Fab M | | \perp | Wafer | Fab Process |
| | | | | Part number | | | | |
| | | | | PCN | <u>Details</u> | | | |
| Descripti | on of C | hange: | | | | | | |
| | | | | announce the quected devices lis | | | | |
| | Cı | ırrent Fa | b Site | е | A | dditio | nal Fa | b Site |
| Current | | Proce | SS | Wafer Diameter | Additional Fab Site | Pı | rocess | Wafer Diameter |
| | DP1DM5 LBC7 | | 7 | 200 mm | RFAB | | LBC7 | 300 mm |
| Assembly Material Differen Mold compound | | | | | | | | |
| M | | | | UTAC SID#CZ0140 | SIC | | SEN 000261 | 141 |
| | | pound | | UTAC | | #180 | | |
| Le | old com eadfram rage, in: | pound e Base sertions, | S | UTAC SID#CZ0140 Standard | Pre-n | #180 nolde | 000261 <mark>d lead</mark> f | |
| Test covertest MQ. Reason f Continuity | old comeadframerage, incorections | pound e Base sertions, nge: ply | condit | UTAC SID#CZ0140 Standard ions will remain | Pre-n consistent with | #18(nolde | 000261 d leadf t testin | rame |
| Test covertest MQ. Reason f Continuity | old comeadframerage, incorections | pound e Base sertions, nge: ply | condit | UTAC SID#CZ0140 Standard ions will remain | Pre-n consistent with | #18(nolde | 000261 d leadf t testin | g and verified with |
| Test cove test MQ. Reason f Continuity Anticipat None | old comeadframerage, in: or Chai of Sup | pound e Base sertions, nge: ply eact on F | condit | UTAC SID#CZ0140 Standard ions will remain | Pre-n consistent with | #18(nolde | 000261 d leadf t testin | g and verified with |
| Test cover test MQ. Reason f Continuity Anticipat None Anticipat De | old comeadfram rage, income or Char or Char of Sup red imp red imp Impact e Materia | pound e Base sertions, nge: ply eact on Ference to al | condition orm, | UTAC SID#CZ0140 Standard sions will remain Fit, Function, Qual Declaration Material Declaration production data release. Upon p | consistent with a consistent w | bility Conteable for the | d leadf t testin (positi | g and verified with ive / negative): orts are driven from g the production |

Fab Site Information:

| Chip Site | Chip Site Origin Code (20L) | Chip Site Country Code (21L) | Chip Site City |
|-----------|--------------------------------|------------------------------|----------------|
| DP1DM5 | DM5 | USA | Dallas |
| RFAB | RFB | USA | Richardson |

Assembly Site Information:

| Assembly Site | Assembly Site Origin (22L) | Assembly Country Code (21L) | Assembly City |
|---------------|----------------------------|-----------------------------|---------------|
| UTAC | NSE | THA | Bangkok |
| ASEN | ASN | CHN | Suzhou |

Sample product shipping label (not actual product label)







Topside Device marking (if included):

Assembly site code for NSE= P
Assembly site code for ASN = W

Product Affected:

Group 1 device list - Qualify both RFAB and ASEN

TS3USB3000MRSER TS3USB3000RSER

Group 2 device list - Qualify only RFAB (ASEN already qualified)

TS3USB3000AMRSER

Qualification Report

TS3USB3000RSER, TS3USB3000MRSE and TS3USB3000AMRSE

Approve Date 13-Apr-2017

Product Attributes

| Attributes | Qual Device: TS3U SB3000R SER | QBS Product Reference: TPS51225C | QBS Product Reference: TS3U SB3000R SER | QBS Product Reference: TS3USB3000RSER | QB\$ Process Reference: ALM2402QDRRRQ1 |
|--------------------|-------------------------------|-------------------------------------|--|--|---|
| Wafer Fab Supplier | RFAB | RFAB | MIHO8 | DP1-DM5 | RFAB |
| Wafer Process | LBC7 | LBC7 | LBC7 | LBC7 | LBC7 |
| Assembly Site | ASEN | CLARK | NSE (UTAC) | ASEN | CLARK |
| Package Family | QFN | QFN | QFN | QFN | SON |

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

| Type | Test Name / Condition | Duration | Qual Device: T S3U SB3000R SER | QBS Product Reference: TPS51225C | QBS Product Reference: TS3USB3000RSER | QBS Product Reference: TS3USB3000RSER | QBS Process Reference: ALM2402QDRRRQ1 |
|------|-------------------------------|-----------------------------------|-----------------------------------|-------------------------------------|--|--|--|
| AC | Autoclave 121C | 96 Hours | | 3/231/0 | - | 3/231/0 | 3/231/0 |
| ED | Electrical Distributions | Cpk>1.67 Room, hot, and cold test | • | - | - | - | 3/90/0 |
| ED | Electrical Characterization | Per Datasheet Parameters | • | Pass | Pass | Pass | - |
| ELFR | Early Life Failure Rate, 150C | 24 Hours | • | - | - | - | 1/800/0 |
| HAST | Biased HAST, 130C/85%RH | 96 Hours | 2/154/0 | 3/231/0 | - | 3/231/0 | 3/231/0 |
| HBM | ESD - HBM | 6000 V | 1/3/0 | - | | 1/3/0 | - |
| CDM | ESD - CDM | 1500 V | 1/3/0 | 2/6/0 | 1/3/0 | - | 1/3/0 |
| HTOL | Life Test, 125C | 1000 Hours | | 3/231/0 | - | | 1/77/0 |
| HTOL | Life Test, 150C | 300 Hours | • | - | - | 1/77/0 | 2/154/0 |
| HTSL | High Temp. Storage Bake, 170C | 420 Hours | • | - | - | 3/231/0 | - |
| HTSL | High Temp. Storage Bake, 150C | 1000 Hours | - | 3/231/0 | - | | 1/45/0 |
| LU | Latch-up | (per JESD78) | 1/6/0 | 2/12/0 | 1/6/0 | 2/12/0 | 1/6/0 |
| PD | Physical Dimensions | - | • | - | - | 3/30/0 | 3/30/0 |
| SD | Surface Mount Solderability | Pb-Free | - | - | - | 3/69/0 | 3/45/0 |
| SD | Surface Mount Solderability | Pb | - | - | - | - | 3/45/0 |
| TC | Temperature Cycle -65C/150C | 500 Cycles | 1/77/0 | 3/231/0 | - | 3/231/0 | 3/231/0 |
| MQ | Manufacturability (Assembly) | (per mfg. Site specification) | Pass | | | | |
| WBP | Bond Pull | Wires | • | - | - | 3/90/0 | 3/228/0 |
| WBS | Ball Bond Shear | Wires | | | - | 3/15/0 | 3/90/0 |
| MSL | Moisture Sensitivity | Level 1, 260C | 1/12/0 | | | | |

⁻ Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

⁻ QBS: Qual By Similarity - Qual Device TS3USB3000RSER is qualified at LEVEL1-260C

⁻ The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles
Quality and Environmental data is available at Ti's external Web site: http://www.ti.com/
Green/Pb-free Status:
Qualified Pb-Free (SMT) and Green



Qualification Report

TS3USB3000RSER and SAP spins TS3USB3000MRSE and TS3USB3000AMRSE Second Source Offload in ASEN Approve Date 17-Feb-2017

Product Attributes

| Die Attributes | Qual Device: TS3USB3000RSER | QBS Process Reference: TPS22932YFP | QBS Process Reference: TPX3110D2PWP |
|--------------------|--------------------------------|---------------------------------------|--|
| Wafer Fab Supplier | DP1-DM5 | DMOS5 | MIHO8 |
| Wafer Process | LBC7 | 3370LBC7 | LBC7 |
| Assembly Site | ASEN | SCS | TAI |
| Package Family | QFN; 2 x1.5MM | WLBGA | TSSOP |

⁻ QBS: Qual By Similarity

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

| Туре | Test Name / Condition | Duration | Qual Device: T S3U SB3000R SER | QBS Process Reference: TPS22932YFP | QBS Process Reference: TPX3110D2PWP |
|------|-------------------------------|--------------------------|-----------------------------------|---------------------------------------|---|
| AC | Autoclave 121C | 96 Hours | 3/231/0 | - | 3/231/0 |
| ED | Electrical Characterization | Per Datasheet Parameters | Pass | Pass | Pass |
| ELFR | Early Life Failure Rate, 150C | 24 Hours | - | - | 3/2400/0 |
| HAST | Biased HAST, 130C/85%RH | 96 Hours | 3/231/0 | - | 3/231/0 |
| HBM | ESD - HBM | 6000 V | 1/3/0 | - | - |
| CDM | ESD - CDM | 1000 V | 1/3/0 | - | 2/6/0 |
| HTOL | Life Test, 150C | 300 Hours | 1/77/0 | 1/77/0 | 3/231/0 |
| HTSL | High Temp Storage Bake 170C | 420 Hours | 3/231/0 | - | 3/231/0 |
| LU | Latch-up | (per JESD78) | 2/12/0 | 1/6/0 | - |
| PD | Physical Dimensions | (per mechanical drawing) | 3/30/0 | - | - |
| SD | Surface Mount Solderability | Pb Free | 3/69/0 | - | - |
| TC | Temperature Cycle, -55/125C | 1000 Cycles | - | 1/77/0 | 3/231/0 |
| TC | Temperature Cycle, -65/150C | 500 Cycles | 3/231/0 | - | 3/231/0 |
| TS | Thermal Shock -65/150C | 500 Cycles | - | - | 3/231/0 |
| WBP | Bond Pull | Wires | 3/90/0 | - | - |
| WBS | Ball Bond Shear | Wires | 3/15/0 | - | - |

⁻ Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

Green/Pb-free Status: Qualified Pb-Free(SMT) and Green

For questions regarding this notice, e-mails can be sent to the regional contacts shown below, or you can contact your local Field Sales Representative.

| Location | E-Mail |
|--------------|--------------------------------|
| USA | PCNAmericasContact@list.ti.com |
| Europe | PCNEuropeContact@list.ti.com |
| Asia Pacific | PCNAsiaContact@list.ti.com |
| Japan | PCNJapanContact@list.ti.com |

⁻ Qual Device TS3USB3000RSER is qualified at LEVEL1-260C

The following are equivalent HTSL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours - The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours - The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/