



N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ.)		
30	0.0027 at V _{GS} = 10 V	36	41 nC		
	0.004 at V _{GS} = 4.5 V	29	41110		

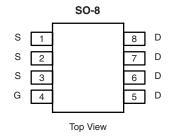
FEATURES

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested

ROHS COMPLIANT HALOGEN FREE Available

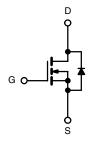
APPLICATIONS

• DC-to-DC and AC-to-DC Oring Diode Applications



Ordering Information: Si4438DY-T1-E3 (Lead (Pb)-free)

Si4438DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise not	ed		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	30	_ v	
Gate-Source Voltage		V _{GS}	± 20		
	T _C = 25 °C		36		
Continuous Dunin Courset /T. 150 °C\	T _C = 70 °C		29		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	24 ^{b, c}		
	T _A = 70 °C		19 ^{b, c}	А	
Pulsed Drain Current		I _{DM}	70		
	T _C = 25 °C	1	7.0		
Continuous Source-Drain Diode Current	T _A = 25 °C	l _S –	3.0 ^{b, c}		
	T _C = 25 °C		7.8		
Maritana Barra Birata di a	T _C = 70 °C		5.0		
Maximum Power Dissipation	T _A = 25 °C	P _D	3.5 ^{b, c}	W	
	T _A = 70 °C		2.2 ^{b, c}		
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R_{thJA}	29	35	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	13	16	C/VV	

Notes:

- a. Based on T_C = 25 °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Maximum under Steady State conditions is 80 $^{\circ}\text{C/W}.$

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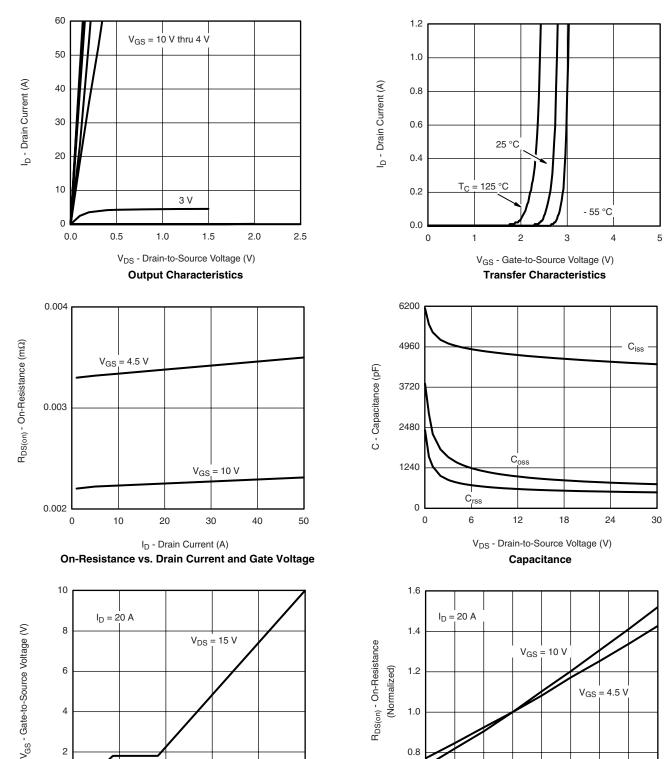
SPECIFICATIONS $T_J = 25 ^{\circ}C$, Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	- Cyzc.	Tool Conditions		.,,,,	III WAI	J 0	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30	T		V	
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J			31		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 6.7			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	1.4	1	2.6	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		1	± 100	nA	
Zero Gate Voltage Drain Current		V _{DS} = 30 V, V _{GS} = 0 V		1	1	μА	
	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C		1	10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V, } V_{GS} = 10 \text{ V}$	30			Α	
_		V _{GS} = 10 V, I _D = 20 A		0.0022	0.0027	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A}$		0.0033	0.004		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 20 A		86		S	
Dynamic ^b				<u> </u>	ı	l	
Input Capacitance	C _{iss}			4645		pF	
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		900			
Reverse Transfer Capacitance	C _{rss}			555			
Tatal Cata Chausa		V _{DS} = 15 V, V _{GS} = 10 V, I _D = 20 A		84	126	nC	
Total Gate Charge	Qg			41	62		
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		14.6			
Gate-Drain Charge	Q_{gd}			16.5			
Gate Resistance	R_g	f = 1 MHz		1.3	2	Ω	
Turn-On Delay Time	t _{d(on)}			36	55		
Rise Time	t _r	V_{DD} = 15 V, R_L = 1.5 Ω		210	320	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ 10 A, V_{GEN} = 4.5 V, R_g = 1 Ω		39	60		
Fall Time	t _f			18	30		
Turn-On Delay Time	t _{d(on)}			17	26		
Rise Time	t _r	V_{DD} = 15 V, R_L = 1.5 Ω		86	130		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ 10 A, V_{GEN} = 10 V, R_g = 1 Ω		47	75		
Fall Time	t _f			10	16		
Drain-Source Body Diode Characteristic	cs						
Continuous Source-Drain Diode Current	I _S	$T_C = 25 ^{\circ}C$			7	A	
Pulse Diode Forward Current ^a	I _{SM}				70		
Body Diode Voltage	V _{SD}	I _S = 3 A		0.73	1.1	V	
Body Diode Reverse Recovery Time	t _{rr}			43	65	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 20 A, dI/dt = 100 A/μs, T _J = 25 °C		45	70	nC	
Reverse Recovery Fall Time	t _a			22		nc	
Reverse Recovery Rise Time	t _b			21		ns	

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



0.6

- 50

- 25

0

25

50

T_J - Junction Temperature (°C)

On-Resistance vs. Junction Temperature

75

100

0

0

17

34

Q_g - Total Gate Charge (nC)

Gate Charge

51

68

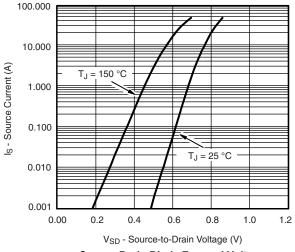
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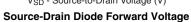
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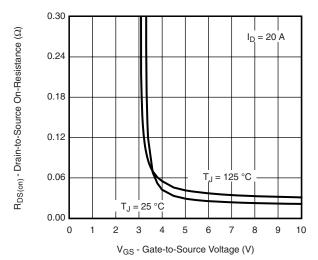
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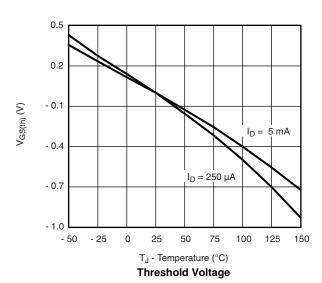
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

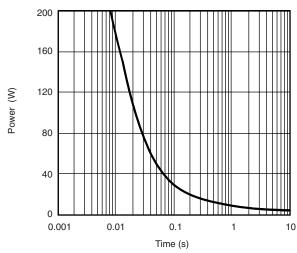




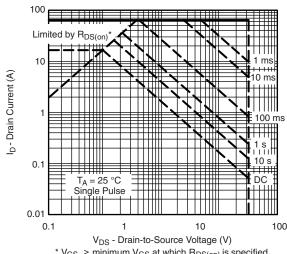


On-Resistance vs. Gate-to-Source Voltage





Single Pulse Power, Junction-to-Ambient

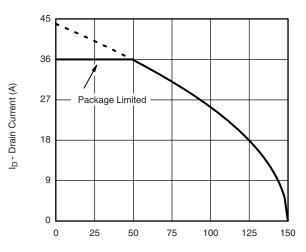


* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

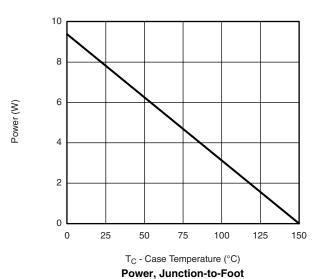


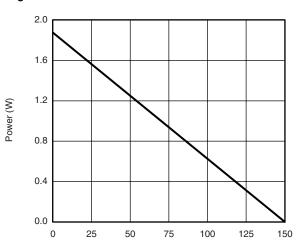
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



T_C - Case Temperature (°C)

Current Derating*





T_A - Ambient Temperature (°C)

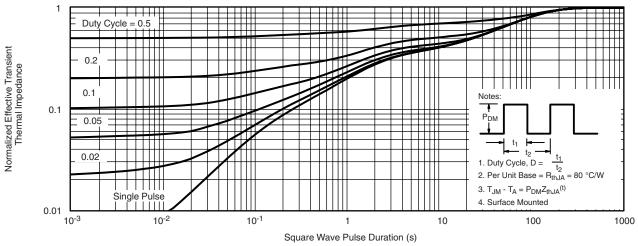
Power, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

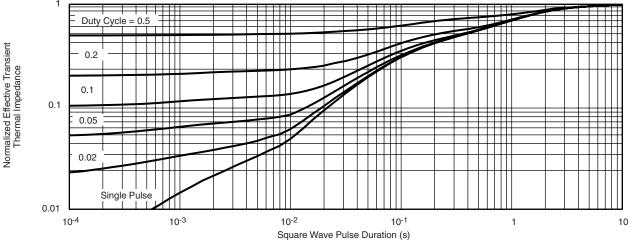
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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