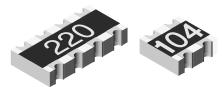




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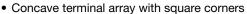
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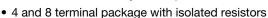
Thick Film Chip Resistor Array



CRA06P thick film resistor array is constructed on a high grade ceramic body with concave terminations. A small package enables the design of high density circuits. The single component reduces board space, component counts and assembly costs.

FEATURES







• Wide ohmic range: 10R to 1M0

• AEC-Q200 qualified

 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

STANDARD ELECTRICAL SPECIFICATIONS									
		0.063	50	100	1	10 to 1M	24 + 96		
CRA06P	03		30	200	2; 5	10 to 1101	24		
		Zero-Ohm-Resisto	or: $R_{\text{max.}} = 50 \text{ m}\Omega$, $I_{\text{max.}}$	= 1 A					

TECHNICAL SPECIFICATIONS							
PARAMETER	UNIT	CRA06P 03 CIRCUIT					
Rated dissipation at 70 °C (2)	W per element	0.063					
Limiting element voltage (1)	V≅	50					
Insulation voltage (1 min)	V _{DC/AC} peak	100					
Category temperature range	°C	-55 to +155					
Insulation resistance	Ω	> 10 ⁹					

Notes

(1) Rated voltage: √P x R

⁽²⁾ The power dissipation on the resistor generates a temperature rise against the local ambient, depending on the heat flow support of the printed-circuit board (thermal resistance). The rated dissipation applies only if the permitted film temperature of 155 °C is not exceeded

•	printed should be and the internal resistance). The rated dissipation applies only in the perinted limit emperature of 166 of 161 oxecoded								
PART NUM	PART NUMBER AND PRODUCT DESCRIPTION								
Part Number: 0	Part Number: CRA06P08347K0JTA (1)								
C R A 0 6 P 0 8 3 4 7 K 0 J T A									
MODEL	TERMINAL STYLE	F	PIN	CIRC	CUIT	VALUE	TOLERANCE	PACKAGING	(2) SPECIAL
CRA06	Р		04	3 =	03	R = decimal	F = ± 1 %	TA	Up to 2 digits
	<u> </u>		80			K = thousand	$G = \pm 2 \%$	TC	
						$\mathbf{M} = \text{million}$ $0000 = 0 \Omega \text{ jumpe}$	$J = \pm 5 \%$ er $Z = 0 \Omega$ jumpe	ar l	
Duadust Dage	intion. CDA06D	. 00 (12 472	L DT4	-0	0000 = 0 32 jumpe	2 = 0 32 jumpe	51	
	iption: CRA06P	00 (es				
CRA06P	08		0:	3		473	J	RT1	e3
MODEL	TERMINAL CO	TAUC	CIRCUI	T TYPE	RESI	STANCE VALUE	TOLERANCE	PACKAGING (4)	LEAD (Pb)-FREE
CRA06P	04		0::1001		473 = 47 kΩ		F = ± 1 %	RT1	e3 = pure tin
OTAGG	J 08			•	$4702 = 47 \text{ k}\Omega$		$G = \pm 2 \%$	RT6	termination finish
$\mathbf{10R0} = 10 \ \Omega$						$J = \pm 5 \%$			
			$100 = 10 \Omega$		$\mathbf{Z} = 0 \Omega$ jumper				
						0 = 0 Ω jumper			
						vo digits (3 for 1 %) Inificant. Last digit			
					the multiplier.				

Notes

(1) Preferred way for ordering products is by use of the PART NUMBER

(2) Please refer to the table PACKAGING, see next page



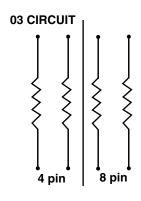


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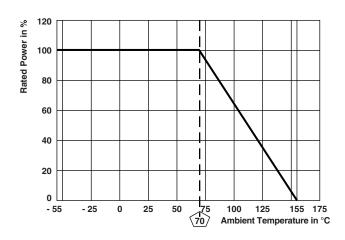
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PACKA	PACKAGING									
			PITCH		PACKAGING CODE PAPER TAPE					
MODEL	TAPE WIDTH	DIAMETER		PIECES/REEL						
					PART NUMBER	PRODUCT DESCRIPTION				
CRAGER	0	180 mm/7"	4 mm	5000	TA	RT1				
CRA06P 8 mm		330 mm/13"	4 mm	20 000	TC	RT6				

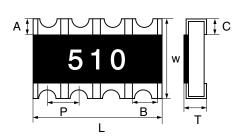
CIRCUIT



DERATING



DIMENSIONS



c [†]		w
b l		<u> </u>

PIN	DIMENSIONS in millimeters								
NO#	L	Α	В	С	P	Т	W		
4	1.60	0.30	0.40	0.40	0.80	0.60	1.60		
8	3.20	0.30	0.40	0.40	0.80	0.60	1.60		
Tol.	± 0.20	± 0.20	± 0.15	± 0.20	-	± 0.10	± 0.15		

SOLDER PAD DIMENSIONS in millimeters								
	c w p a b							
WAVE	0.8	2.6	0.8	0.4	0.9			



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CRA06P

 $\pm (2 \% R + 0.1 \Omega)$

 $\pm (4 \% R + 0.1 \Omega)$

 $\pm~(2~\%~R+0.1~\Omega)$

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	EN 60115-1			
TEST	CONDITIONS OF TEST	REQUIREMENTS PERMISSIBLE CHANGE ($\Delta R/R$) (1)		
(clause)	CONDITIONS OF TEST	STABILITY CLASS 1 OR BETTER	STABILITY CLASS 2 OR BETTER	
	Stability for product types:	10.0 to 1.M0	10.0 to 1.M0	
	CRA06P	10 Ω to 1 MΩ	10 Ω to 1 MΩ	
Resistance (4.5)	-	± 1 %	± 2 %; ± 5 %	
Temperature coefficient (4.8.4.2)	(20 / -55 / 20) °C and (20 / 125 / 20) °C	± 100 ppm/K	± 200 ppm/K	
Overload (4.13)	$U = 2.5 \times (P_{70} \times R)^{1/2}$ $\leq 2 \times U_{\text{max.}}; 0.5 \text{ s}$	± (0.25 % R + 0.05 Ω)	$\pm (0.5 \% R + 0.05 \Omega)$	
Solderability (4.17.5) (2)	Aging 4 h at 155 °C, dryheat Solder bath method; 235 °C; 2 s Visual examination	Good tinning (≥ 95 % covered) no visible damage		
Resistance to soldering heat (4.18.2)	Solder bath method; (260 ± 5) °C; (10 ± 1) s	± (0.25 % R + 0.05 Ω)	± (0.5 % R + 0.05 Ω)	
Rapid change of temperature (4.19)	30 min at LCT = -55 °C; 30 min at UCT = 125 °C; 5 cycles	± (0.25 % R + 0.05 Ω)	$\pm (0.5 \% R + 0.05 \Omega)$	
Damp heat, steady state (4.24)	(40 ± 2) °C; 56 days; (93 ± 3) % RH	± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)	
Climatic sequence (4.23)	16 h at UCT = 125 °C; 1 cycle at 55 °C; 2 h at LCT = -55 °C; 1 h/1 kPa at 15 °C to 35 °C; 5 cycles at 55 °C	± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)	

 $U = (P_{70} \times R)^{1/2}$ $U = U_{\text{max.}}; \text{ whichever is less severe}$ $U = (P_{70} \times R)^{1/2}$ $U = U_{\text{max.}}; \text{ whichever is less severe}$

1.5 h "ON"; 0.5 h "OFF"; 70 °C; 1000 h

Duration extended to 8000 h

UCT = 125 °C; 1000 h

 $\pm (1 \% R + 0.05 \Omega)$

 $\pm (2 \% R + 0.1 \Omega)$

 $\pm (1 \% R + 0.05 \Omega)$

Notes

Endurance at 70 °C (4.25.1)

Extended endurance (4.25.1.8)

Endurance at upper category

temperature (4.25.3)

APPLICABLE SPECIFICATIONS

EN 60115-1 Generic specification
 EN 140400 Sectional specification
 EN 140401-802 Detail specification

• IEC 60068-2-X Variety of environmental test procedures

• EIA 481 Packaging of SMD components

⁽¹⁾ Figures are given for a single element

⁽²⁾ Solderability is specified for 2 years after production or requalification. Permitted storage time is 20 years



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