



Product Change Notification / SYST-08VWPS152

Date:

16-Jun-2022

Product Category:

General Purpose FPGAs, System On Chip FPGAs

PCN Type:

Document Change

Notification Subject:

Data Sheet - AN4153 Board and Layout Design Guidelines for SmartFusion2 SoC and IGLOO2 FPGAs
Data Sheet Document Revision

Affected CPNs:

[SYST-08VWPS152_Affected_CPN_06162022.pdf](#)
[SYST-08VWPS152_Affected_CPN_06162022.csv](#)

Notification Text:

SYST-08VWPS152

Microchip has released a new Datasheet for the AN4153 Board and Layout Design Guidelines for SmartFusion2 SoC and IGLOO2 FPGAs of devices. If you are using one of these devices please read the document located at [AN4153 Board and Layout Design Guidelines for SmartFusion2 SoC and IGLOO2 FPGAs](#).

Notification Status: Final

Description of Change: The following is a summary of changes made in this revision: 1. Updated 1.1.2. Power Supply Sequencing. 2. Updated 1.2.2. I/O Glitch During Power-Down.

For more information, see [CAN-CN FPGA: SmartFusion2/IGLOO2 power down sequence to prevent eNVM corruption](#)

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 16 Jun 2022

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices::N/A

Attachments:

[AN4153 Board and Layout Design Guidelines for SmartFusion2 SoC and IGLOO2 FPGAs](#)

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Affected Catalog Part Numbers (CPN)

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M2S005S-1VFG256T2
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