High Directivity Monolithic Amplifier

MNA-5A+

50Ω 0.5 to 2.5 GHz

The Big Deal

- · Integrated matching, DC Blocks and bias circuits
- Excellent Active Directivity
- Operates over 2.8-5V

Product Overview

MNA-5A+ is a wideband PHEMT based MMIC amplifier with high active Directivity. MNA integrates the entire matching network and majority of the bias circuit inside the package, reducing the need for complicated external circuits. This approach makes the MNA amplifier extremely straightforward to use. This design operates on a single 2.8 to 5V supply, is well matched for 50Ω and comes in a tiny, low profile 3x3mm 8-lead MCLP package accommodating dense circuit board layouts.

Key Features

Feature	Advantages		
Excellent Active Directivity (Isolation- Gain) 15-22 dB	Ideal for use as a buffer amplifier minimizing interaction of adjacent circuits		
Integrates DC blocks and RF choke	Minimizes external components, component count and circuit area.		
Single 2.8 to +5V operation	Amplifier can be used at low voltage such as +3V or standard +5V. +5V operation results in higher P1dB and OIP3.		
3 x 3mm 8-lead MCLP package	Tiny footprint saves space in dense layouts while providing low inductance, repeatable transitions, and excellent thermal contact to the PCB.		



CASE STYLE: DQ849

High Directivity Monolithic Amplifier

0.5-2.5 GHz

Product Features

- Choice of supply voltage, 2.8V to 5V
- Micro-miniature size .120"X.120"
- Internal DC blocking at RF input and output
- High directivity, 15-22 dB typ.
- Low noise figure, 3.1 dB typ. at 2 GHz
- Output power, up to +14 dBm typ.
- Aqueous washable

Generic photo used for illustration purposes only MNA-5A+

CASE STYLE: DQ849

+RoHS Compliant The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

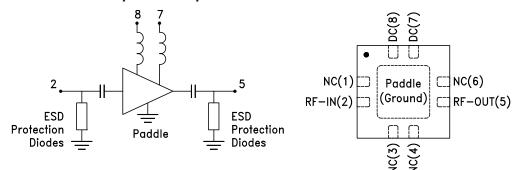
Typical Applications

- Buffer amplifier
- Cellular
- PCN
- Communications satellite
- Defense

General Description

MNA-5A+ is a wideband PHEMT based MMIC amplifier with high active Directivity. MNA integrates the entire matching network and majority of the bias circuit inside the package, reducing the need for complicated external circuits. This approach makes the MNA amplifier extremely straightforward to use. This design operates on a single 2.8 to 5V supply, is well matched for 50Ω and comes in a tiny, low profile 3x3mm 8-lead MCLP package accommodating dense circuit board layouts.

simplified schematic and pad description



Function	Pad Number	Description (See Fig 1)
RF-IN	2	RF input pin
RF-OUT	5	RF output pin
DC	7,8	DC Bias pads 7,8. Pad 7 connected to ground via 1000 pF. Pad 8 connected to pad 7 via 33 ohms.
NC	1,3,4,6	Not Connected, connect pad 3 and 4 to ground externally
GND	Paddle	Ground
OPTIONAL	1,6	No internal connection; recommended use: per PCB Layout PL-078

REV. OR M154121 MNA-5A+ RS/CP 200930 Page 2 of 5

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Electrical Specifications¹ at 25°C

Parameter	Condition (GHz)		Vs=5V		Vs=2.8V	Units
Falameter		Min.	Тур.	Max.	Тур.	
Frequency Range		0.5		2.5	0.5-2.5	GHz
Gain	0.5	_	21.6	_	20.5	dB
	0.75	_	23.3	_	21.9	
	1.0	_	23.5	_	21.9	
	1.5	_	22.8	_	21.0	
	2.0	19.1	21.3	23.4	19.4	
	2.5		19.2	_	17.4	
Input Return Loss	0.5		5.4		5.6	dB
	0.75		10.4		10.8	
	1.0		14.9		15.3	
	1.5		21.4		20.8	
	2.0		21.6		21.1	
	2.5		17.1		17.2	
Output Return Loss	0.5		14.5		14.4	dB
	0.75		15.4		23.1	
	1.0		12.8		17.4	
	1.5		11.6		15.3	
	2.0		12.2		15.3	
	2.5		12.8		15.7	
Output Power at P1dB	0.5		12.6		11.0	dBm
	0.75		12.0		10.8	dDin
	1.0		11.1		10.0	
	1.5		10.4		9.4	
	2.0		9.6		8.4	
	2.5		8.7		7.5	
Output IP3	0.5		23.8		21.8	dBm
	0.75		23.8		21.7	dDin
	1.0		22.3		20.6	
	1.5		21.3		19.6	
	2.0		20.1		18.5	
	2.5		19.1		17.5	
Noise Figure	0.5		3.2		3.3	dB
Noise i igure	0.75		3.3		3.2	UD UD
	1.0		3.0		3.0	
	1.5		3.0		3.1	
	2.0		3.1		3.1	
			3.2		3.2	
Directivity	2.5 0.5		20.3	-	22.5	dB
(Isolation-Gain)	0.5		20.3		22.5	UD
	1.0		19.7		19.3	
	1.5		17.4		16.9	
	2.0		16.6		16.2	
	2.5		17.3	10	16.6	
DC Current		_	34	43	31.8	mA
Device Current Variation vs. Temperature ⁽²⁾			15		5	µA/°C
Device Current Variation vs Voltage			0.00043		0.00144	mA/mV
Thermal resitance at 85°C	d TB-186+. See Characterizatio		64		64	°C/W

Measured on Mini-Circuits Characterization test board TB-186+. See Characterization Test Circuit (Fig. 1)
 (Current at 85°C - Current at -45°C)/130
 (Current at 5.25V-Current at 3.9V)/1.35
 (Current at 3.9V-Current at 2.66V)/1.24

Absolute Maximum Ratings⁵

Parameter	Ratings
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
DC Voltage	7V at pad 7 (on TB-186+)
	1V at pads 2 & 5
Power Dissipation	700 mW
Input Power	5 dBm (continuous operation)
	28 dBm (5 minutes max)

5. Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation



Characterization & Application Test Circuit

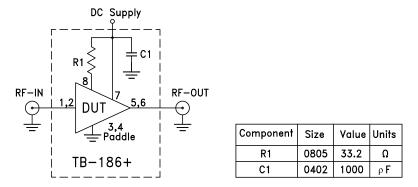


Fig 1. Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization test board TB-313) Gain, Return loss, Output power at 1dB compression (P1 dB), output IP3 (OIP3) and noise figure measured using Agilent's N5242A PNA-X microwave network analyzer.

Conditions:

- 1. Gain and Return loss: Pin= -25dBm
- 2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, -5dBm/tone at output.

Recommended Application Circuit

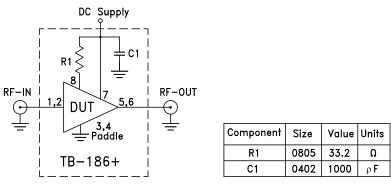


Fig 2. Test Board includes case, connectors, and components soldered to PCB

Product Marking





Additional Detailed Technical Information additional information is available on our dash board. To access this information <u>click here</u>				
	Data Table			
Performance Data	Swept Graphs			
	S-Parameter (S2P Files) Data Set (.zip file)			
Case Style	DQ849 3x3x0.9 mm MCLP Plastic package, exposed paddle lead finish: Matte-Tin			
Tape & Reel	F104			
Standard quantities available on reel	7" reels with 20, 50, 100, 200, 500, 1K, or 2K devices			
Suggested Layout for PCB Design	PL-078			
Evaluation Board	TB-186-5A+			
Environmental Ratings	ENV08T1			

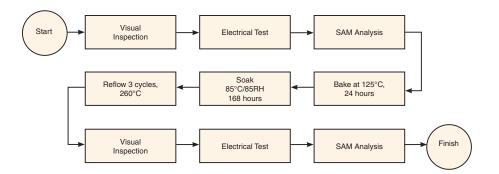
ESD Rating

Human Body Model (HBM): Class 1A (250 to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): Class M1 (>25V) in accordance with ANSI/ESD STM5.2-1999

MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D



Additional Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

